

**DEC 13 2007****TROP, PRUNER & HU, P.C.**

INTELLECTUAL PROPERTY LAW ATTORNEYS

1616 South Voss Road, Suite 750

Houston, Texas 77057-2631

Bus: (713) 468-8880

Fax: (713) 468-8883

**Fax**

<b>To:</b>	Examiner C. Anyikire	<b>From:</b>	Timothy N. Trop
<b>Company:</b>	US Patent Office	<b>Date:</b>	December 13, 2007
<b>Fax:</b>	(571) 273-8300	<b>Pages:</b>	7 (including cover sheet)
<b>Your Re:</b>	10/750,075	<b>Our Re:</b>	ITL.1703US

☐ **Urgent**    ☐ **For Review**    ☐ **Please Comment**    ☐ **Please Reply**    ☐ **Confirm Receipt**

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Louis Lippincott et al.	§	Art Unit:	2621
Serial No.:	10/750,075	§	Examiner:	Chikaodili E. Anyikire
Filed:	December 31, 2003	§	Docket:	ITL.1703US P17498
For:	Motion Estimation Sum of all Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption	§	Assignee:	Intel Corporation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed November 7, 2007, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made to the claims.

Date of Deposit: December 13, 2007  
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden